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**Comments:**

Patent Number 6,917,934

1940 Zanker Rd. San Jose, CA 95112

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PTO/SB/123 (01-06)

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Patent Number	6917934
Issue Date	07/12/2005
Application Number	10/065,261
Filing Date	09/30/2002
First Named Inventor	Cheng, Paul
Attorney Docket Number	ALNC01500

Please change the Correspondence Address for the above-identified patent to:

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- ☒ Assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).
- ☐ Attorney or agent of record. Registration Number \_\_\_\_\_

**Signature**
**Typed or Printed Name** Lou Yang
**Date****Telephone** 408-969-5124

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☐ \*Total of \_\_\_\_\_ forms are submitted.

This collection of information is required by 37 CFR 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Post Issue, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/96 (08-08)

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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Integrated Silicon Solution, Inc.Application No./Patent No.: 6,917,934Filed/Issue Date: issued 07/12/2005Entitled: Segmented Bus Architecture For Improving Speed In Integrated Circuit MemoriesIntegrated Silicon Solution, Inc.a Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest  
(The extent (by percentage) of its ownership interest is \_\_\_\_\_ %)

in the patent application/patent identified above by virtue of either:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Cheng, Paul C. et al.To: Integrated Silicon Solution, Inc.

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2. From: Integrated Silicon Solution, Inc.To: Integrated Silicon Solution, Inc.

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☐ Additional documents in the chain of title are listed on a supplemental sheet.

☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Signature

Lou Yang

Printed or Typed Name

V.P. of Business Development

Title

Date

8/14/08

Telephone Number

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